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<u>L21</u>	L17 and 18	11	<u>L21</u>
<u>L20</u>	L17 and 17	20	<u>L20</u>
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<u>L8</u>	(711/201, 214) [CCLS]	275	<u>L8</u>

<u>L7</u>	(711/201-221)![CCLS]	5134	<u>L7</u>
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<u>L5</u>	(712/2-300)![CCLS]	10675	<u>L5</u>
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<u>L4</u>	align\$8 near12 (word\$1 or byte\$1 or 32 or thirty near1 two) near25 address\$3 near8 (generat\$3 or calculat\$4 or comput\$4)	219	<u>L4</u>
<u>L3</u>	L2 near15 (conflict\$3 or contend\$3 or contention\$1 or collaps\$5 or hazard\$5 or dependen\$5)	236	<u>L3</u>
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<u>L1</u>	(command\$3 or instruction\$1 or micro near1 instruction\$1) near 5 (fetch\$6 or prefetch\$6)	103749	<u>L1</u>

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1 The microarchitecture of superscalar processors
Smith, J.E.; Sohi, G.S.;

Proceedings of the IEEE , Volume: 83 , Issue: 12 , Dec. 1995

Pages:1609 - 1624

[\[Abstract\]](#) [\[PDF Full-Text \(1812 KB\)\]](#) **IEEE JNL**
2 A 330 MHz 4-way superscalar microprocessor
Greenhill, D.; Anderson, E.; Bauman, J.; Charnas, A.; Cheerla, R.; Hao Chen; Doreswamy, M.; Ferolito, P.; Gopaladhine, S.; Ho, K.; Wenjay Hsu; Kongetira; Melanson, R.; Reddy, V.; Salem, R.; Sathianathan, H.; Shah, S.; Shin, K.; Srivatsa, C.; Weisenbach, R.;

Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC 1997 IEEE International , 6-8 Feb. 1997

Pages:166 - 167, 449

[\[Abstract\]](#) [\[PDF Full-Text \(1104 KB\)\]](#) **IEEE CNF**
3 A 7th-generation x86 microprocessor
Hesley, S.; Andrade, V.; Burd, B.; Constant, G.; Correll, J.; Crowley, M.; Gola M.; Hopkins, N.; Islam, S.; Johnson, S.; Khondker, R.; Meyer, D.; Moench, J.; Partovi, H.; Posey, R.; Weber, F.; Yong, J.;

Solid-State Circuits Conference, 1999. Digest of Technical Papers. ISSCC. 1999 IEEE International , 15-17 Feb. 1999

Pages:92 - 93

[\[Abstract\]](#) [\[PDF Full-Text \(144 KB\)\]](#) **IEEE CNF**

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